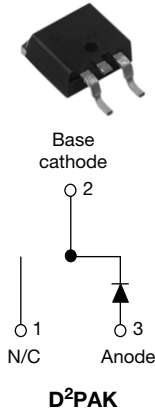
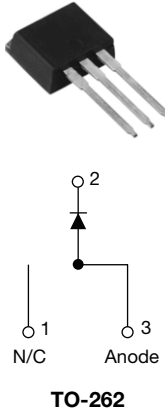


Ultrafast Rectifier, 8 A FRED Pt®

VS-8ETU04SPbF



VS-8ETU04-1PbF



FEATURES

- Ultrafast recovery time
- Low forward voltage drop
- Low leakage current
- 175 °C operating junction temperature
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Halogen-free according to IEC 61249-2-21 definition
- Compliant to RoHS directive 2002/95/EC
- AEC-Q101 qualified



RoHS
COMPLIANT
HALOGEN
FREE

DESCRIPTION/APPLICATIONS

Vishay HPP's FRED Pt® series are the state of the art ultrafast recovery rectifiers specifically designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, dc-to-dc converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

PRODUCT SUMMARY

t_{rr}	60 ns
$I_{F(AV)}$	8 A
V_R	400 V

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Repetitive peak reverse voltage	V_{RRM}		400	V
Average rectified forward current	$I_{F(AV)}$	$T_C = 155\text{ °C}$	8	A
Non-repetitive peak surge current	I_{FSM}	$T_C = 25\text{ °C}$	100	
Repetitive peak forward current	I_{FRM}		16	
Operating junction and storage temperatures	T_J, T_{Stg}		- 65 to 175	°C

ELECTRICAL SPECIFICATIONS ($T_J = 25\text{ °C}$ unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V_{BR}, V_R	$I_R = 100\ \mu\text{A}$	400	-	-	V
		$I_F = 8\text{ A}$	-	1.19	1.3	
Forward voltage	V_F	$I_F = 8\text{ A}, T_J = 150\text{ °C}$	-	0.94	1.0	μA
		$V_R = V_R\text{ rated}$	-	0.2	10	
Reverse leakage current	I_R	$T_J = 150\text{ °C}, V_R = V_R\text{ rated}$	-	20	500	pF
		$V_R = 400\text{ V}$	-	14	-	
Junction capacitance	C_T	$V_R = 400\text{ V}$	-	14	-	nH
Series inductance	L_S	Measured lead to lead 5 mm from package body	-	8.0	-	nH

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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Reverse recovery time	t _{rr}	I _F = 1.0 A, di _F /dt = 50 A/μs, V _R = 30 V	-	35	60	ns	
		T _J = 25 °C	-	43	-		
		T _J = 125 °C	-	67	-		
Peak recovery current	I _{RRM}	I _F = 8 A di _F /dt = 200 A/μs V _R = 200 V	T _J = 25 °C	-	2.8	-	A
			T _J = 125 °C	-	6.3	-	
Reverse recovery charge	Q _{rr}	I _F = 8 A di _F /dt = 200 A/μs V _R = 200 V	T _J = 25 °C	-	60	-	nC
			T _J = 125 °C	-	210	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		- 65	-	175	°C
Thermal resistance, junction to case	R _{thJC}		-	1.8	2.0	°C/W
Thermal resistance, junction to ambient	R _{thJA}	Typical socket mount	-	-	50	
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.5	-	
Weight			-	2.0	-	g
			-	0.07	-	oz.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style D ² PAK	8ETU04S			
		Case style TO-262	8ETU04-1			



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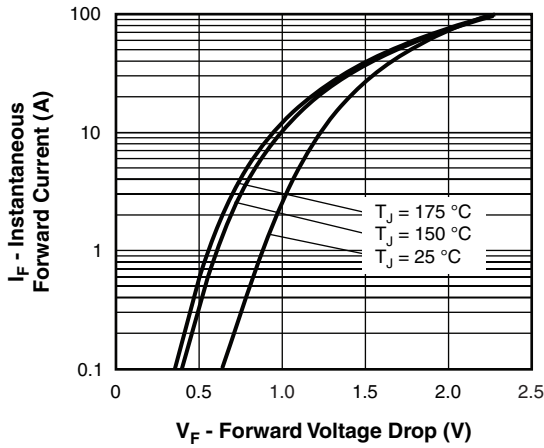


Fig. 1 - Typical Forward Voltage Drop Characteristics

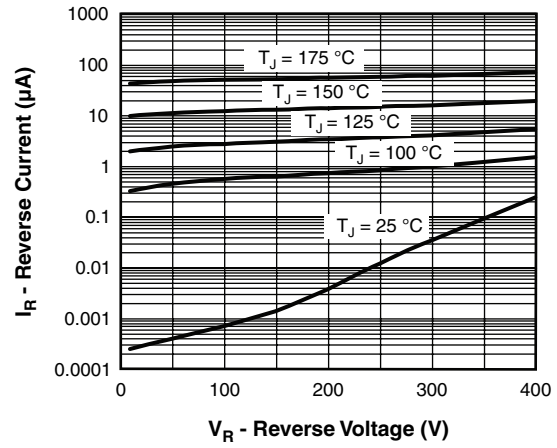


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

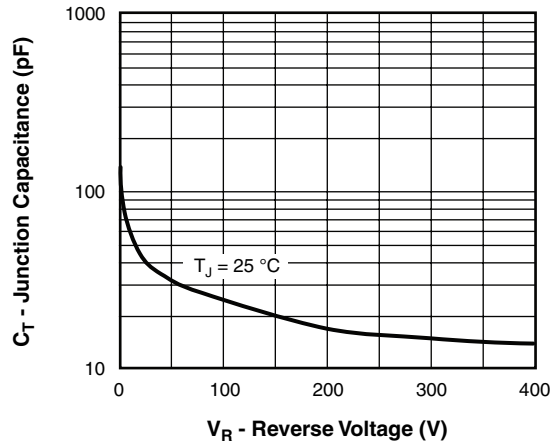


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

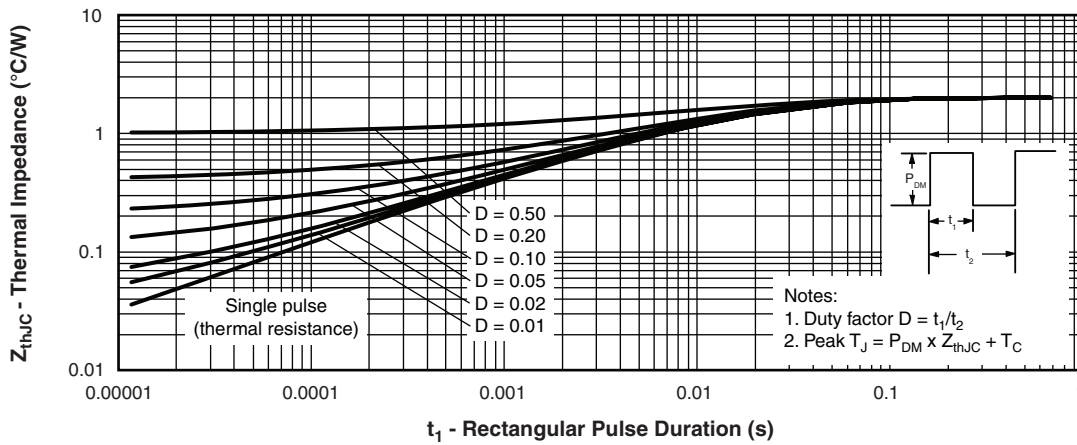


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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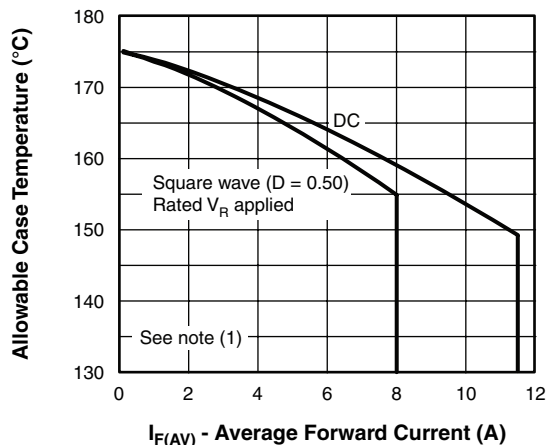


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

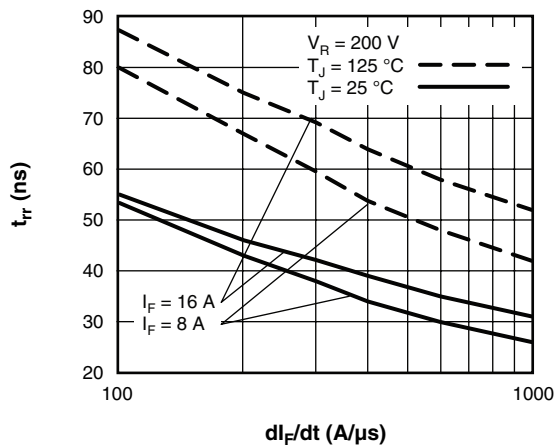


Fig. 7 - Typical Reverse Recovery Time vs. di_F/dt

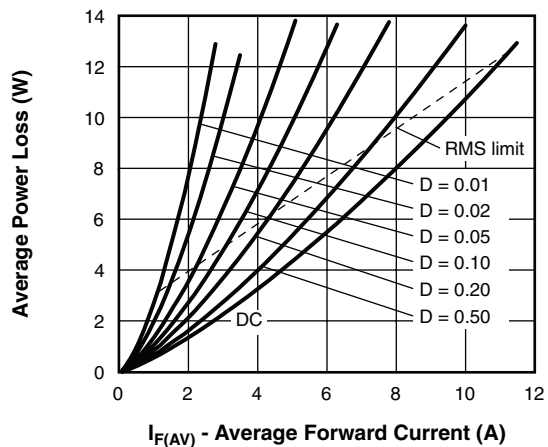


Fig. 6 - Forward Power Loss Characteristics

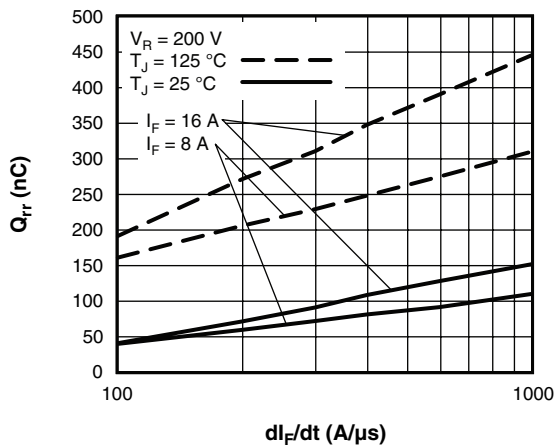


Fig. 8 - Typical Stored Charge vs. di_F/dt

Note

- (1) Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;
 Pd = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6);
 Pd_{REV} = Inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at V_{R1} = Rated V_R

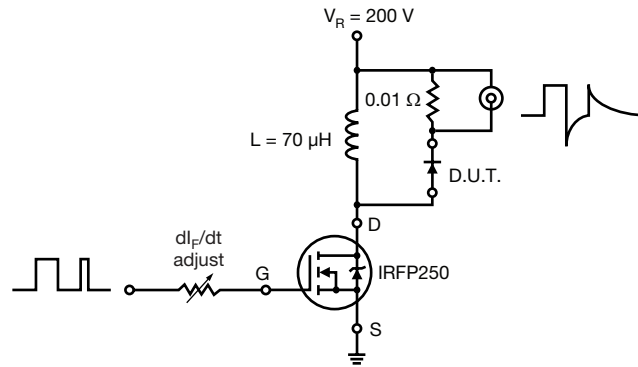
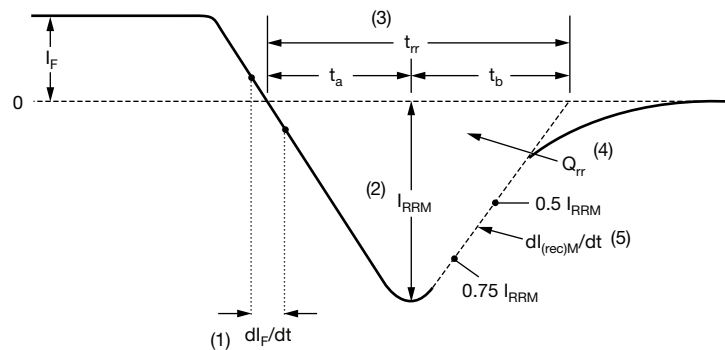


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) di_F/dt - rate of change of current through zero crossing
- (2) I_{RRM} - peak reverse recovery current
- (3) t_{rr} - reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through $0.75 I_{RRM}$ and $0.50 I_{RRM}$ extrapolated to zero current.
- (4) Q_{rr} - area under curve defined by t_{rr} and I_{RRM}
- (5) $di_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

Fig. 10 - Reverse Recovery Waveform and Definitions

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ORDERING INFORMATION TABLE

Device code	VS-	8	E	T	U	04	S	TRL	PbF
	①	②	③	④	⑤	⑥	⑦	⑧	⑨

- 1** - HPP product suffix
- 2** - Current rating (8 A)
- 3** - E = Single diode
- 4** - T = TO-220, D²PAK
- 5** - U = Ultrafast recovery
- 6** - Voltage rating (04 = 400 V)
- 7** -
 - S = D²PAK
 - -1 = TO-262
- 8** -
 - None = Tube (50 pieces)
 - TRL = Tape and reel (left oriented, for D²PAK package)
 - TRR = Tape and reel (right oriented, for D²PAK package)
- 9** - PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS	
Dimensions	www.vishay.com/doc?95014
Part marking information	www.vishay.com/doc?95008
Packaging information	www.vishay.com/doc?95032



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